

# Power Factor Improvement of a Three-Phase Induction Motor using Shunt Active Power Filter

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**Abstract**—This paper describes the feasibility of unity power factor operation for three-phase induction motor subjected to variable loads. In this paper a shunt active power filter is used to provide such operation. The principle of operation is introduced. The two-loop control strategy is used to analyze. From simulation results it is clear that, under all loading conditions, the power factor is achieved unity automatically. Furthermore, the supply current can be obtained sinusoidal. A system based on microcontroller has been tested in the lab to confirm the analysis. The experimental results show that this method to improve power factor of motor is feasible.

**Keywords:** power factor, Three Phase Induction motor, active power filter

## 1. INTRODUCTION

Induction motors are commonly used in domestic, commercial and industrial applications. The power factor of induction motors is usually low and depends upon the load. If the motor is subjected to a fixed load, the power factor of the motor can be improved by a single fixed capacitor connected across the terminals of the motor. For variable load conditions, the power factor can be improved by using switched capacitors. But such a scheme provides capacitance variation in discrete steps and there is over compensation occurs therefore this method is not optimum. A Thyristor switched capacitor (TSC) type of static compensator is used to improve power factor of motor [1]. It can be used to improve the power factor of motors, but the cubage and weight of the system is too large to be implied in the small motor system.

This paper describes a method in which the power factor of induction motors can be improved by using a shunt active power filter. In this system a three-phase pulse width modulated (PWM) voltage source inverter connected to independently controlled dc bus. The size of the system is small. Therefore it can be integrated with motors and provides a unity power factor operation under varying load condition. The model of the system is given in this paper and computer simulation is completed. Simulation results indicate that this kind of system can operate with unity power factor of three phase induction motor.

## 2. POWER FACTOR IMPROVEMENT BY USING CAPACITOR BANK:

The power factor improvement method employing a capacitor bank is shown in fig. 1

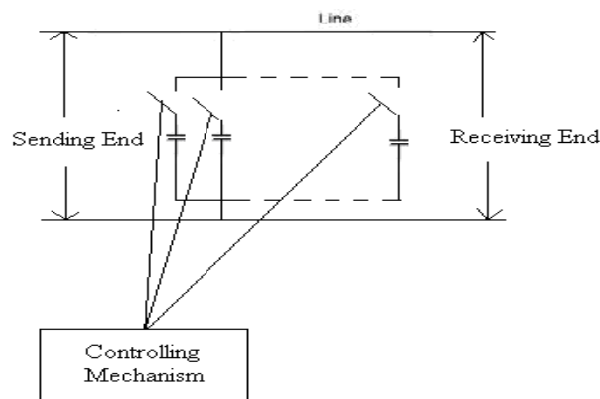


Fig. 1: Pf improvement using capacitor bank

In this method a program is written to add capacitor in steps according to the reactive power requirement.

## 3. POWER FACTOR IMPROVEMENT BY USING THYRISTOR SWITCHED CAPACITOR (TSC)

The power factor improvement method employing a "Thyristor Switched Capacitor" is illustrated in fig. 2

In this control method the capacitor energy supply can be controlled by thyristor. When the firing angle of the Thyristor is varied, then voltage supplied by the capacitor will be changed so that the reactive power supplied by the capacitor will also be varied. A resistance is connected in series with the capacitor to limit the current flowing through the capacitor. The main purpose of resistor is to protect the capacitor from damages due to the high current.

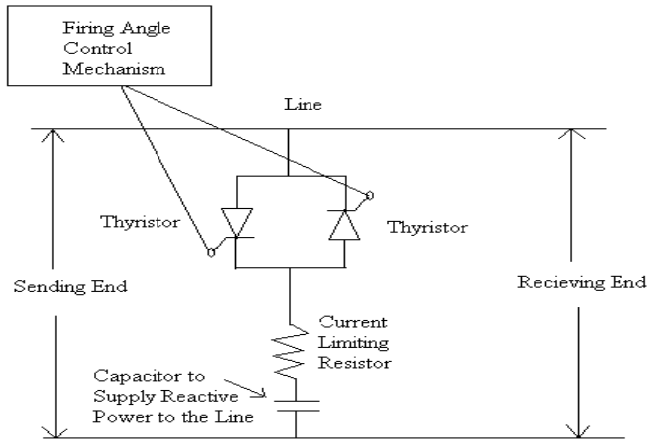


Fig. 2: Pf improvement using TSC

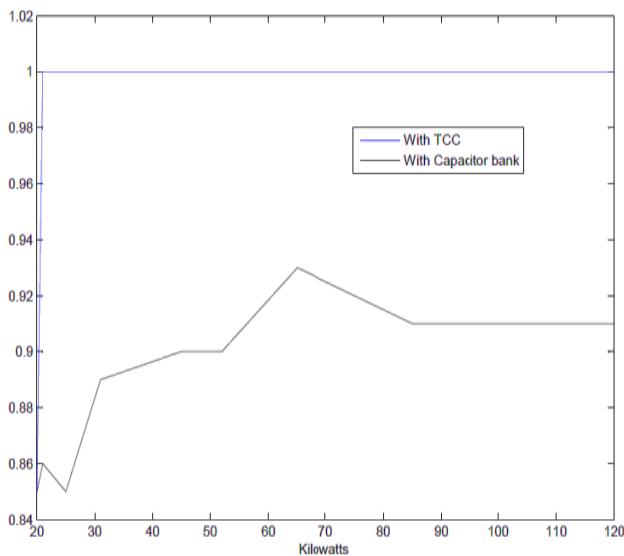


Fig. 3: Power factor comparison graph

**4. POWER FACTOR IMPROVEMENT BY USING SHUNT ACTIVE POWER FILTER:**

It is a device used in parallel with the load. It compensates the current harmonics in the line. It also helps in reactive power compensation in the line. Due to injection of current harmonics it improves the power factor and increases the efficiency of the system and also reduces the losses caused by the harmonics [2].

**Basic Working Principle**

The schematic diagram of a shunt active power filter is shown in Fig. 4. The source current is  $i_s$ ,  $i_L$  is load current,  $i_c$  is compensating current. The source is a balanced voltage source where the phase voltages are transferred to a nonlinear

load. When the SAPF block is not operating, the nonlinear load current is itself the source current, which causes degradation in power factor and introduces harmonic distortion. When the SAPF block is operating, it injects a current  $i_c$  equal in magnitude but in phase opposition to the harmonic current in each of the lines. This current compensates the harmonic distortion and makes the source current a balanced sinusoid, while the load current remains nonlinear.

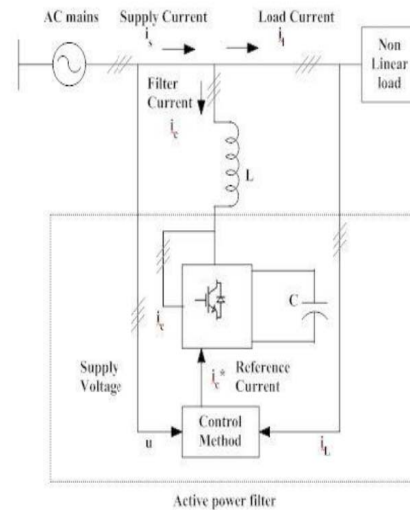


Fig. 4: Schematic diagram of shunt connected active power filter

This SAPF block is basically divided into two parts. The first part is a control block and the second part is a converter, either a VSI with a capacitor source or a CSI with an inductive source. The control block performs necessary computations and operations to generate the compensation current reference [3, 4].

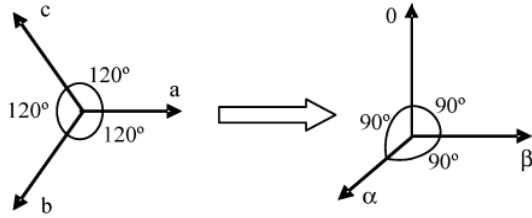
Control Methods used in Active Power Filter are:

- synchronous detection method
- Synchronous Reference Current Theory (d-q Theory).
- Instantaneous Power Theory (P-Q Theory)
- Notch filter method

Above all these methods are used in the development of an Active Power Filter [4]. In this paper, we only discuss two control methods of an Active Power Filter.

**5. DIGITAL CONTROL OF SAPF BASED ON P-Q THEORY**

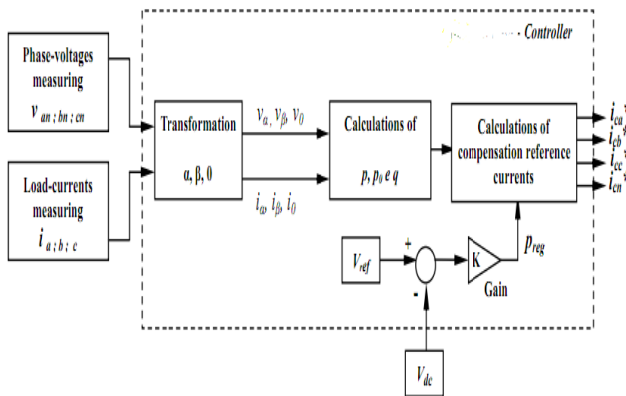
The instantaneous reactive power theory is the most widely used as a control strategy for the APF. It is mainly applied to compensation equipment in parallel connection. This theory is based on a Clarke coordinate transformation from the phase co-ordinates (see Fig. ).



**Fig. 5: Transformation from the phase reference system (ab c) to the (0αβ) system [5].**

It is described a method of calculating the instantaneous power components from sometransformed values of three-phase voltages and currents. The only desirable quantity among all the power components obtained through the p-q theory is real power (p) because it corresponds to the energy transferred from the supply to the load. To compensate all the other quantities SAPF is used. This theory represented an easy way to compensate real power by delivering it from the power source to the active filter through α-β coordinates, so that the active filter is supply this power to the load through the 0 coordinates in a balanced way [3].

The p-q theory calculations are done in the shunt active power filter controller block [6].



**Fig. 7: Calculations of the p-q theory**

The controller allows, in a organized way, according to the received information it verify the needing of compensation currents by the active filter. The controller receives the information of phase voltages, load currents and DC voltage, and based on its control algorithm, proceeds to the calculations of the p-q theory values, generating, or not, the necessary reference compensation currents, which are injected in the power system by the inverter block.

$$\begin{bmatrix} i_{ca}^* \\ i_{cb}^* \\ i_{cc}^* \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1/\sqrt{2} & 1 & 0 \\ 1/\sqrt{2} & -1/2 & \sqrt{3}/2 \\ 1/\sqrt{2} & -1/2 & -\sqrt{3}/2 \end{bmatrix} \cdot \begin{bmatrix} i_{c0}^* \\ i_{ca}^* \\ i_{c\beta}^* \end{bmatrix}$$

When the above currents are fed to the VSI it generates the exact same of these currents but in magnitude equal to the original line currents. These compensated currents are injected to the system to obtain in-phase sinusoidal source currents with the line voltages that improved the power factor in the system[3].

**6. CONTROL OF SAPF SYSTEM USING SYNCHRONOUS DETECTION METHOD (SDM):**

In the SDM, it is assumed that the three-phase main currents are balanced after compensation, and it tries to determine that required amplitude of the main currents. The instantaneous nonlinear load currents and corresponding phase voltages are measured from the lines and real power P(t) is calculated from them Then the average value Pdc is determined by applying P(t) to a low pass filter. Thus, the real power is split into the three phases P<sub>a</sub>, P<sub>b</sub>, P<sub>c</sub>

The compensations current references are

$$\begin{aligned} i_{ca}^* &= i_{sa} - i_a \\ i_{cb}^* &= i_{sb} - i_b \\ i_{cd}^* &= i_{sc} - i_c \end{aligned}$$

The compensation current references i<sup>\*</sup><sub>ca</sub>, i<sup>\*</sup><sub>cb</sub> and i<sup>\*</sup><sub>cc</sub> are given to the VSI which supplies the exact same of these currents I<sub>ca</sub>, i<sub>cb</sub> and I<sub>cc</sub> to the line. Since the PWM VSI is assumed to be instantaneous to track the compensation currents, it is work as a current amplifier with unity gain [3].

Faster power factor improvement is a basic requirement for industrial and consumer equipment’s and SAPF offers better performance than other compensation methods. It improves power factor by significantly reducing the harmonic components in currents. The digital control of SAPF based on p-q theory provides faster power factor improvement than SDM technique. In summing, digital control of SAPF should be the preferred choice for power factor improvement.

**7. CONCLUSIONS**

In this paper a power factor compensation system for three-phase induction motor has been presented. The proposed system has employed an active power filter to be connected to the supply with induction motor directly. The performance of two-loop control strategy is analyzed through transfer function. The system is modeled and simulated to provide theoretical reference. The experimental system based on microcomputer 80C552 is built and tested. Experimental results show that motor will operate with unity power factor, at the same time, the ac supply provides only active current and transmission loss of electrical wire is decreased.

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